

(PRIOR ART)

FIG. 1

09766735, 012201

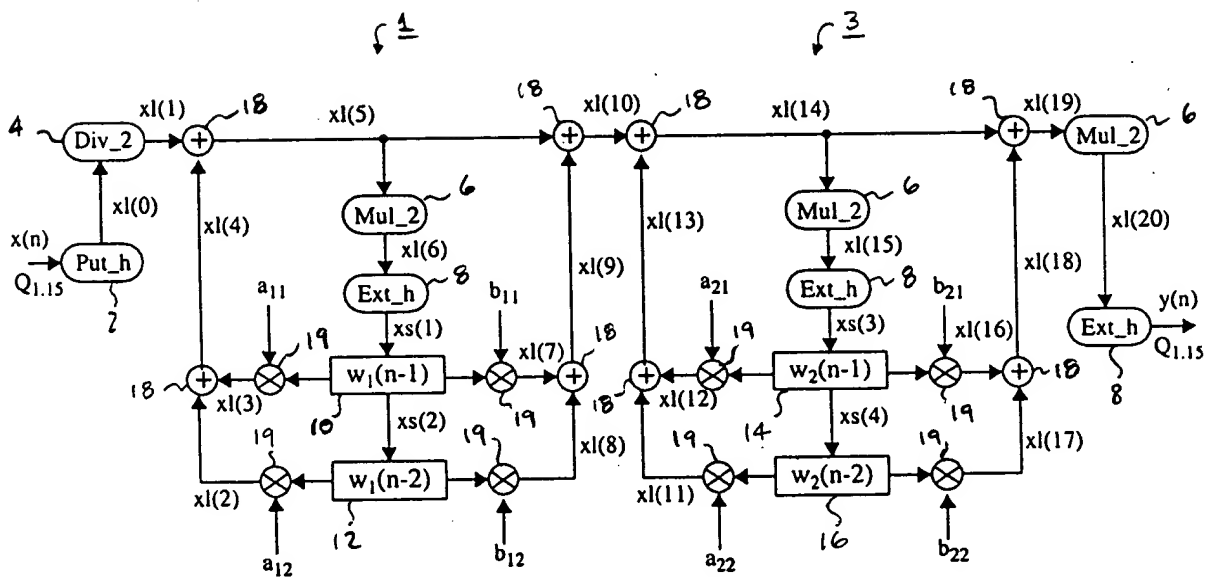
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20 → asr d0,d0      move.4f (r0)+,d4:d5:d6:d7      move.4f (r1),d8:d9:d10:d11
22 → mac d6,d8,d0    mpy d4,d8,d1      move.4f (r0),d12:d13:d14:d15
24 → mac d7,d9,d0    mac d5,d9,d1      mpy d14,d10,d2      mpy d12,d10,d3
26 → mac d15,d11,d2  mac d13,d11,d3   add d0,d0,d0      add d1,d1,d1
28 → add d0,d1,d10   add d2,d2,d2      add d3,d3,d3      tfr d10,d7
30 → add d10,d2,d6   tfr d0,d4      move.f (r1),d5
32 → add d6,d3,d0    moves.4f d4:d5:d6:d7,(r1)

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(PRIOR ART)

FIG. 2



(PRIOR ART)

FIG. 3

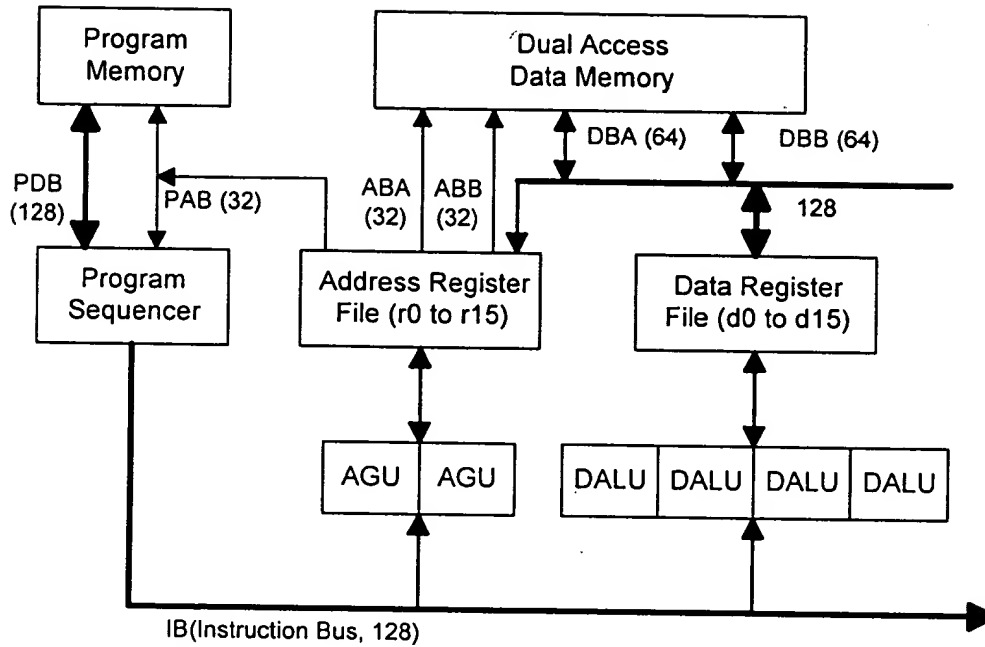
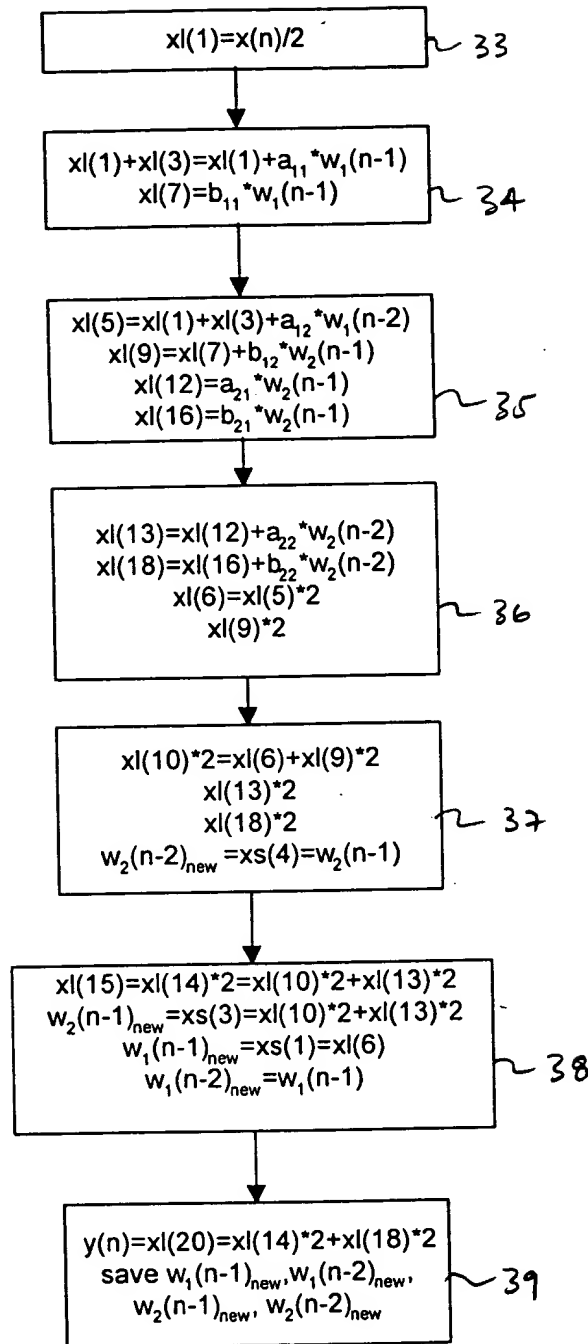


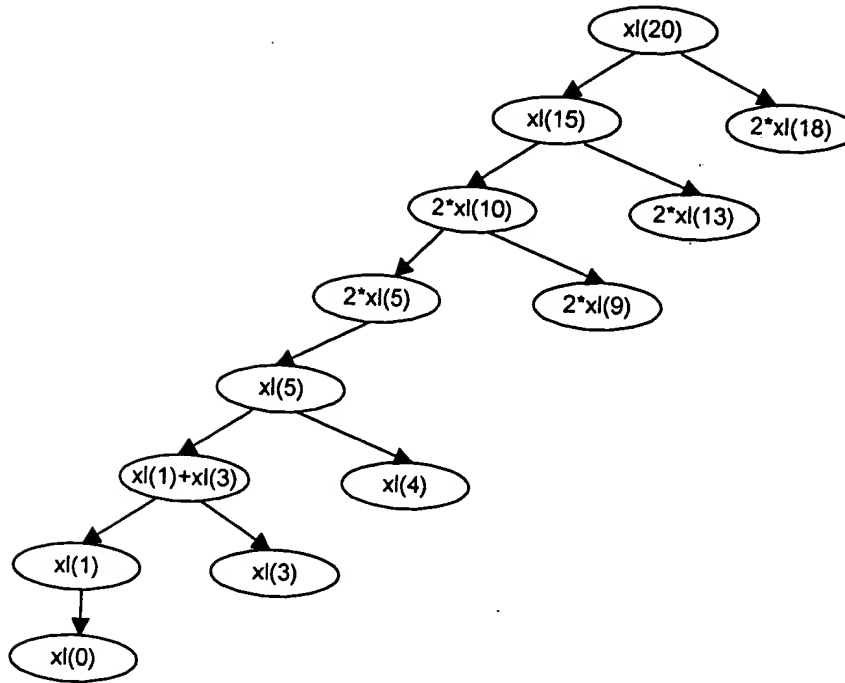
FIG. 4

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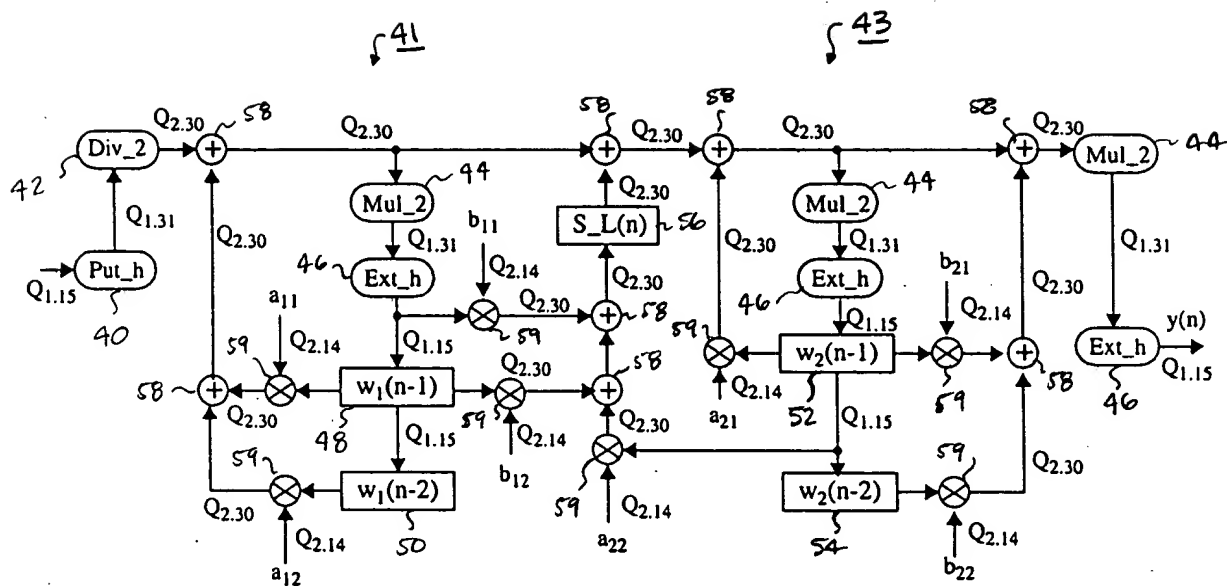


(PRIOR ART)
FIG. 5

09766735-01201



(PRIOR ART)
FIG. 6



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60 → asr d0,d0      move.4f (r0)+,d4:d5:d6:d7      move.4f (r1)+,d8:d9:d10:d11
62 → mac d7,d9,d0    mpy d5,d8,d3      tfr d8,d9      move.l (r1),d2      move.4f (r0),d12:d13:d14:d15
64 → mac d6,d8,d0    mac d14,d10,d2    mac d15,d10,d3    mpy d13,d11,d1    tfr r1,r0
66 → add d0,d0,d8    add d0,d2,d2      mac d12,d10,d1    tfr d10,d11      suba #8,r0
68 → add d2,d1,d0    add d2,d2,d10    mac d4,d8,d3
70 → add d0,d0,d0    move.l d3,(r1)    moves.4f d8:d9:d10:d11,(r0)

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FIG. 8

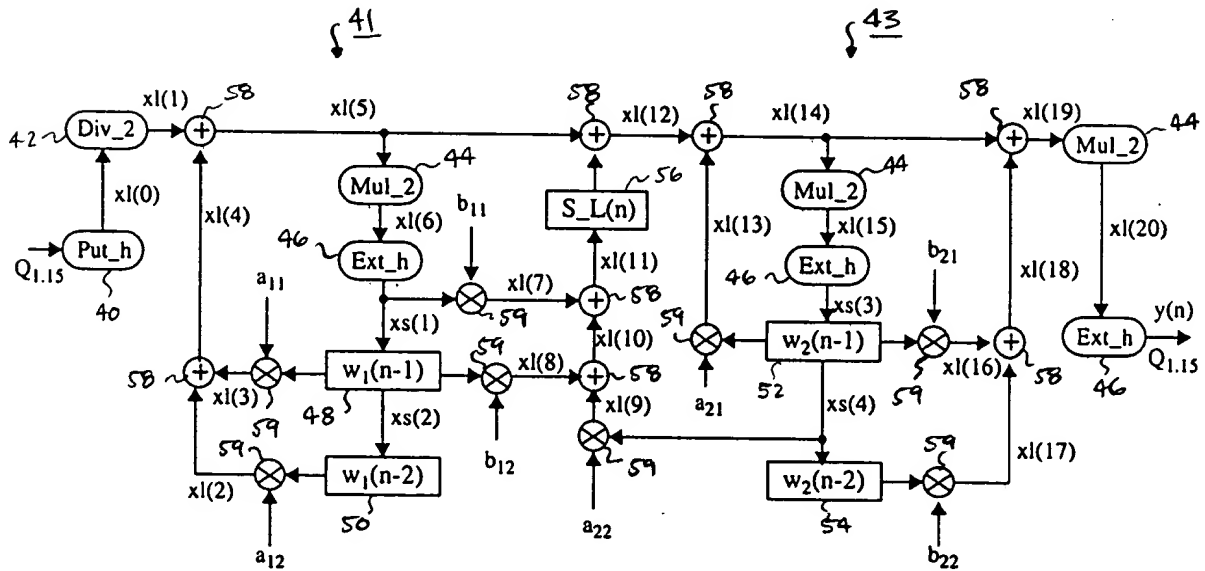


FIG. 9

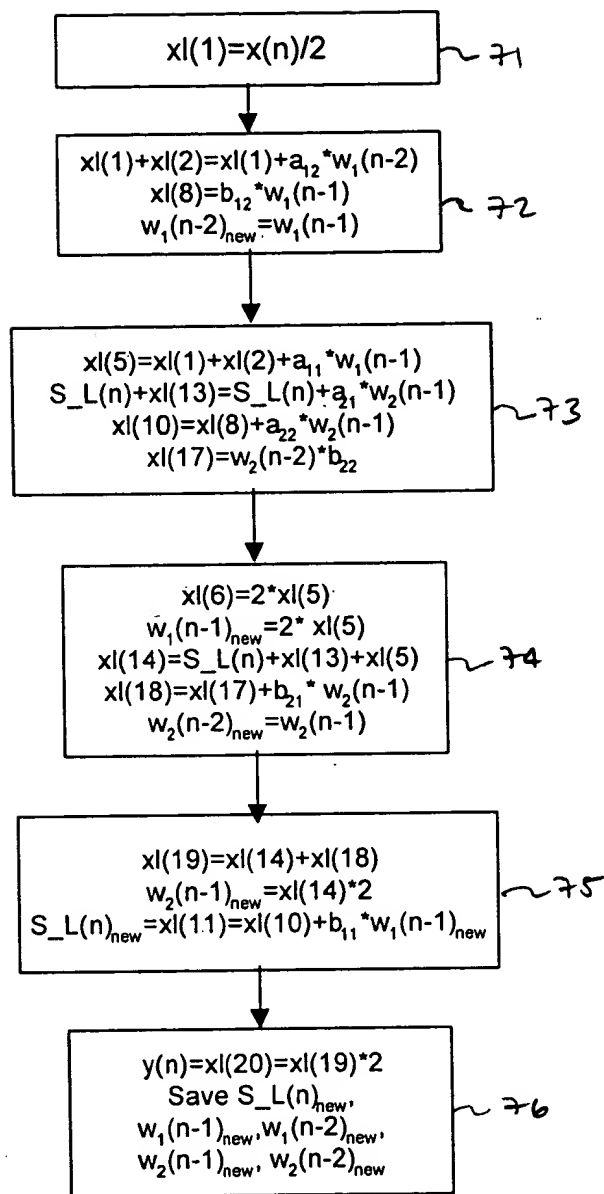


FIG. 10

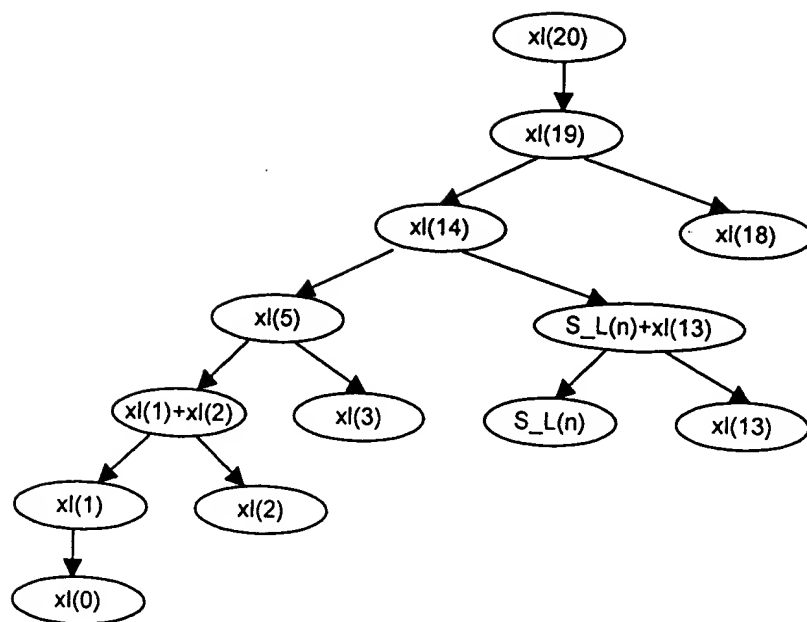


FIG. 11